## KVR26N19S6/8

8GB 1Rx16 1G x 64-Bit PC4-2666

## CL19 288-Pin DIMM

## DESCRIPTION

This document describes ValueRAM's KVR26N19S6/8 is a 1 Gx 64-bit (4GB) DDR4-2666 CL19 SDRAM (Synchronous DRAM), 1Rx16, memory module, based on four 1G x 16-bit FBGA components. The SPD is programmed to JEDEC standard latency DDR4-2666 timing of 19-19-19 at 1.2V. Each 288-pin DIMM uses gold contact fingers. The electrical and mechanical specifications are as follows:

## FEATURES

- Power Supply: VDD = 1.2V Typical
- $\mathrm{VDDQ}=1.2 \mathrm{~V}$ Typical
- $\mathrm{VPP}=2.5 \mathrm{~V}$ Typical
- $\mathrm{VDDSPD}=2.2 \mathrm{~V}$ to 3.6 V
- Nominal and dynamic on-die termination (ODT) for data, strobe, and mask signals
- Low-power auto self refresh (LPASR)
- Data bus inversion (DBI) for data bus
- On-die VREFDQ generation and calibration
- Single-rank
- On-board 12 serial presence-detect (SPD) EEPROM
- 8 internal banks; 2 groups of 4 banks each
- Fixed burst chop (BC) of 4 and burst length (BL) of 8 via the mode register set (MRS)
- Selectable BC4 or BL8 on-the-fly (OTF)
- Fly-by topology
- Terminated control command and address bus
- PCB: Height 1.23 " $(31.25 \mathrm{~mm})$
- RoHS Compliant and Halogen-Free

SPECIFICATIONS

| CL(IDD) | 19 cycles |
| :--- | :--- |
| Row Cycle Time (tRCmin) | $45.75 \mathrm{~ns}(\mathrm{~min})$. |
| Refresh to Active/Refresh <br> Command Time (tRFCmin) | $350 \mathrm{~ns}(\mathrm{~min})$. |
| Row Active Time (tRASmin) | $32 \mathrm{~ns}(\mathrm{~min})$. |
| UL Rating | $94 \mathrm{~V}-0$ |
| Operating Temperature | $0^{\circ} \mathrm{C}$ to $+85^{\circ} \mathrm{C}$ |
| Storage Temperature | $-55^{\circ} \mathrm{C}$ to $+100^{\circ} \mathrm{C}$ |

## MODULE DIMENSIONS



All measurements are in millimeters.
(Tolerances on all dimensions are $\pm 0.12$ unless otherwise specified)


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